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D. Dufur
3-21-02

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Nobuaki HASHIMOTO

Application No.: New Rule 1.53(b) Continuation of Application No. 09/589,353

Filed: November 26, 2001

Docket No.: 103092.02

For: SEMICONDUCTOR DEVICE AND METHOD OF MANUFACTURING
THE SAME, CIRCUIT BOARD, AND ELECTRONIC INSTRUMENT

JC986 U.S. PTO
09/991931
11/26/01

INFORMATION DISCLOSURE STATEMENT

Director of the U.S. Patent and Trademark Office
Washington, D.C. 20231

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- ☒ 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- ☒ 2. The references were cited by or submitted to the Office in parent application No. 09/589,353, filed June 8, 2000, which is relied upon for an earlier filing date under 35 U.S.C. §120. Thus, copies of these references are not attached. 37 CFR §1.98(d).

Respectfully submitted,



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